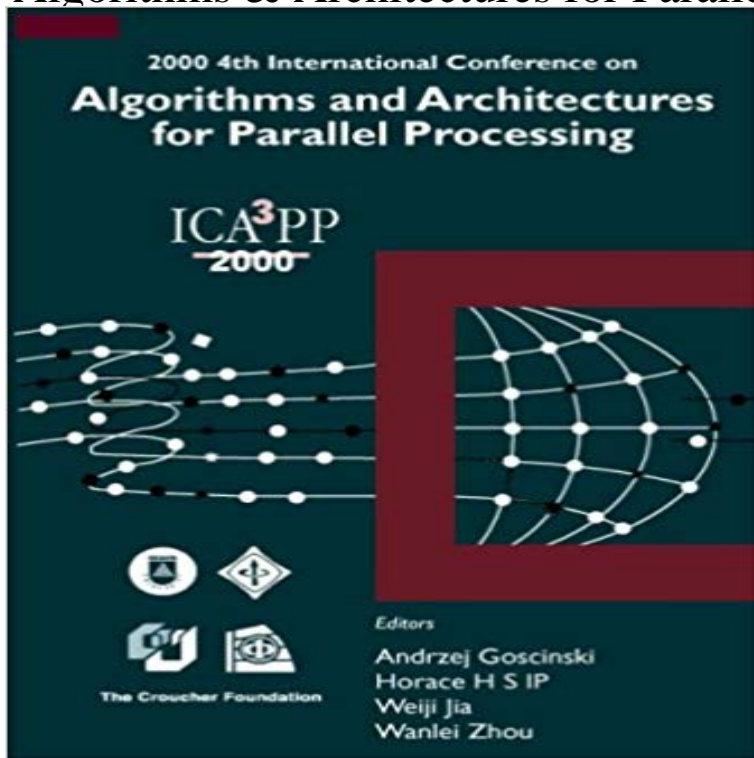


Algorithms & Architectures for Parallel Processing, 4th Intl Conf



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